



**JADAVPUR UNIVERSITY**  
**IC Design & Fabrication Centre**

Electronics & Telecommunication Engineering Department  
Kolkata – 700032.

**Admission for Diploma in VLSI-2015**

JU announces a Diploma course in VLSI System, Duration: 6 Months. Time: 12 Noon to 4 PM, 4 days/week. **Eligibility:** B.E / B.Tech (in Electronics / Computer Science / Instrumentation / Electrical Engineering) or M.Sc , B.Sc.(H) in Electronics / Computer Science or equivalent (AMIE/AMIETE etc.), Diploma in Electrical / Electronics / Computer Science / Instrumentation. Apply in prescribed form available in our website [[www.juiccentre.in](http://www.juiccentre.in)]. Last date of Application: 24<sup>th</sup> July 2015. Written test on 27<sup>th</sup> July 2015, Final list on 30<sup>th</sup> July 2015, Admission within 3<sup>rd</sup> August to 7<sup>th</sup> August 2015, Class will be starting from 14<sup>rd</sup> August 2015. **Fees:** @ Rupees **20,000/-** in demand draft should be drawn in favor of “**REGISTRAR, JADAVPUR UNIVERSITY**”, in any branches of “**State Bank of India**” payable at Kolkata. Hostel accommodation cannot be provided.

**Course Content:-** Details of IC Design & Fabrication Process and practice of different EDA Tools like Tanner Tools, ISE Design Suite (VHDL, FPGA), Analog & Digital Devices & MOS Physics.

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**Last date of Application: 24<sup>th</sup> July 2015.**